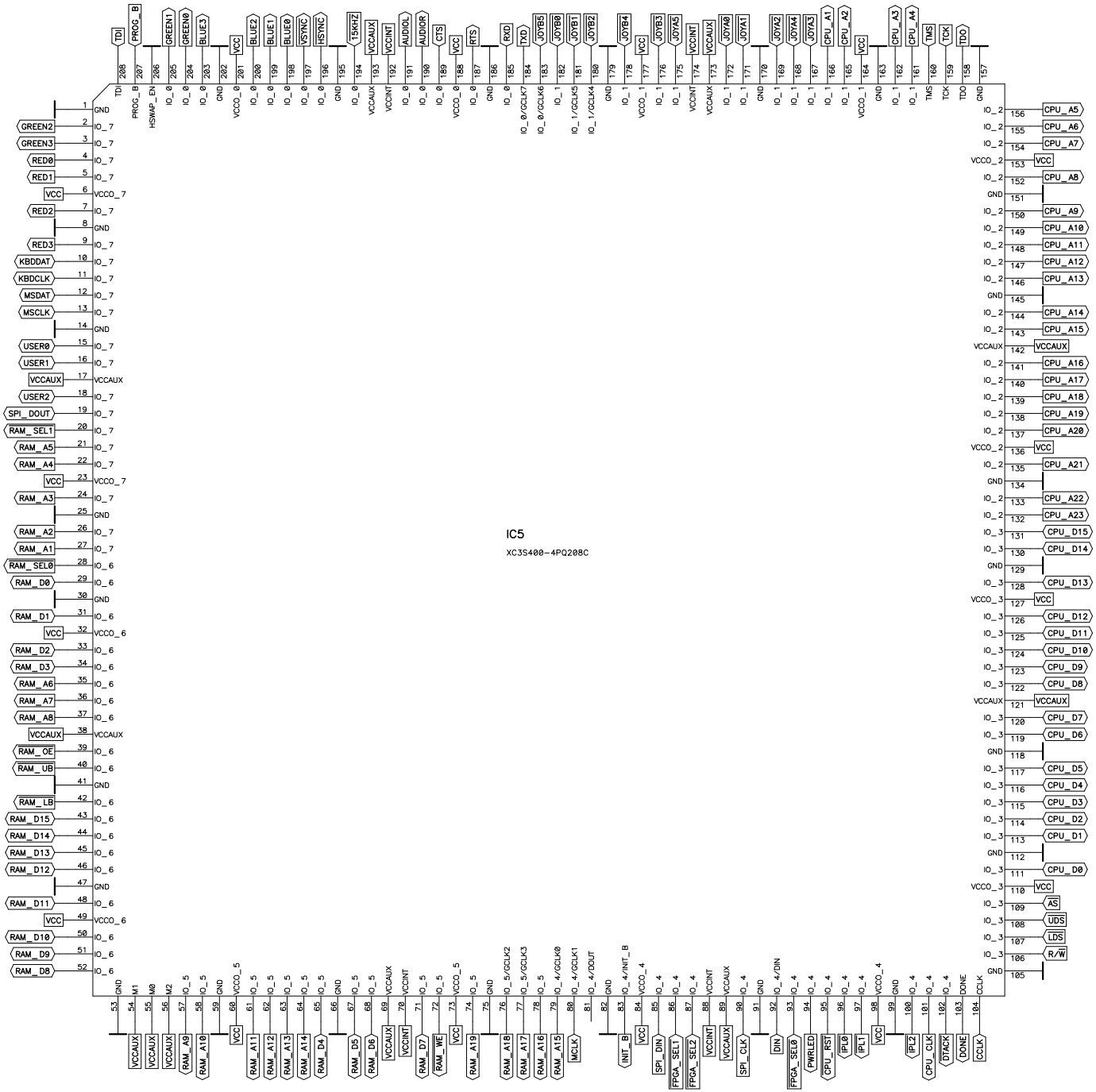
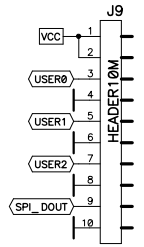
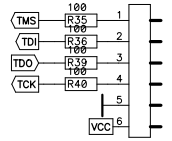


Changes from REV 1.0:
* SPI_DOUT is now connected to pin 19 of the FPGA
* PS/2 connectors shielding is now connected to frame ground
* R49 is now 100 Ohm
* no more wire patches needed!

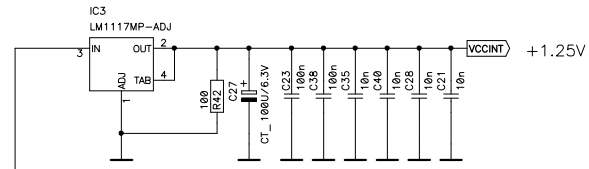
Spare I/O



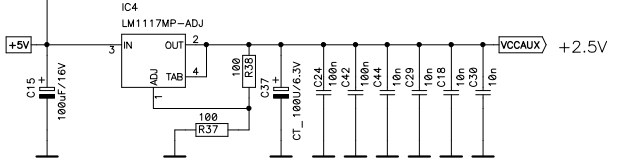
JTAG



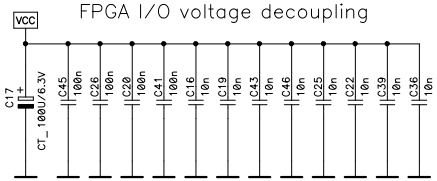
FPGA core voltage decoupling



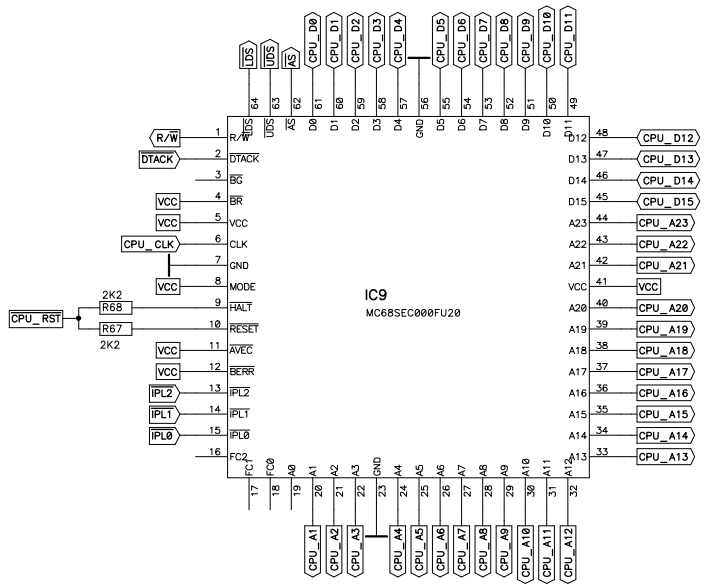
FPGA auxiliary voltage decoupling

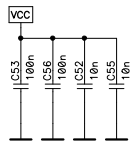


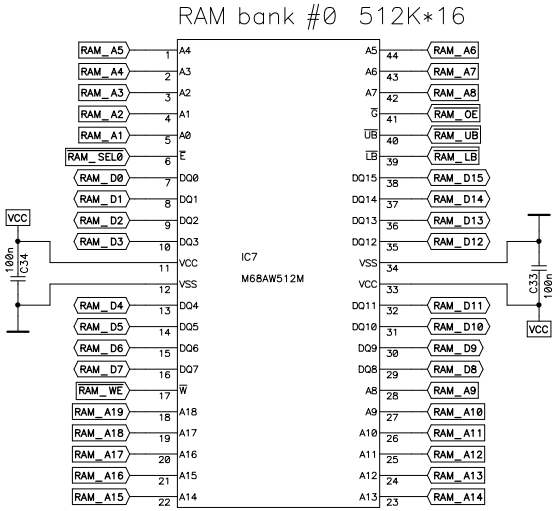
FPGA I/O voltage decoupling

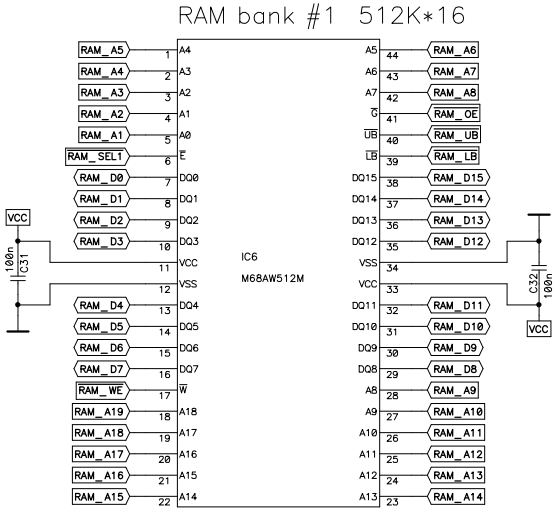


Title	MINIMIG REV 1.1	
Size A2	Number SPARTAN POWER	Rev 2
Date 03-10-2007		Drawn by Dennis van Weeren
Filename Minimg11		Sheet 1 of 3

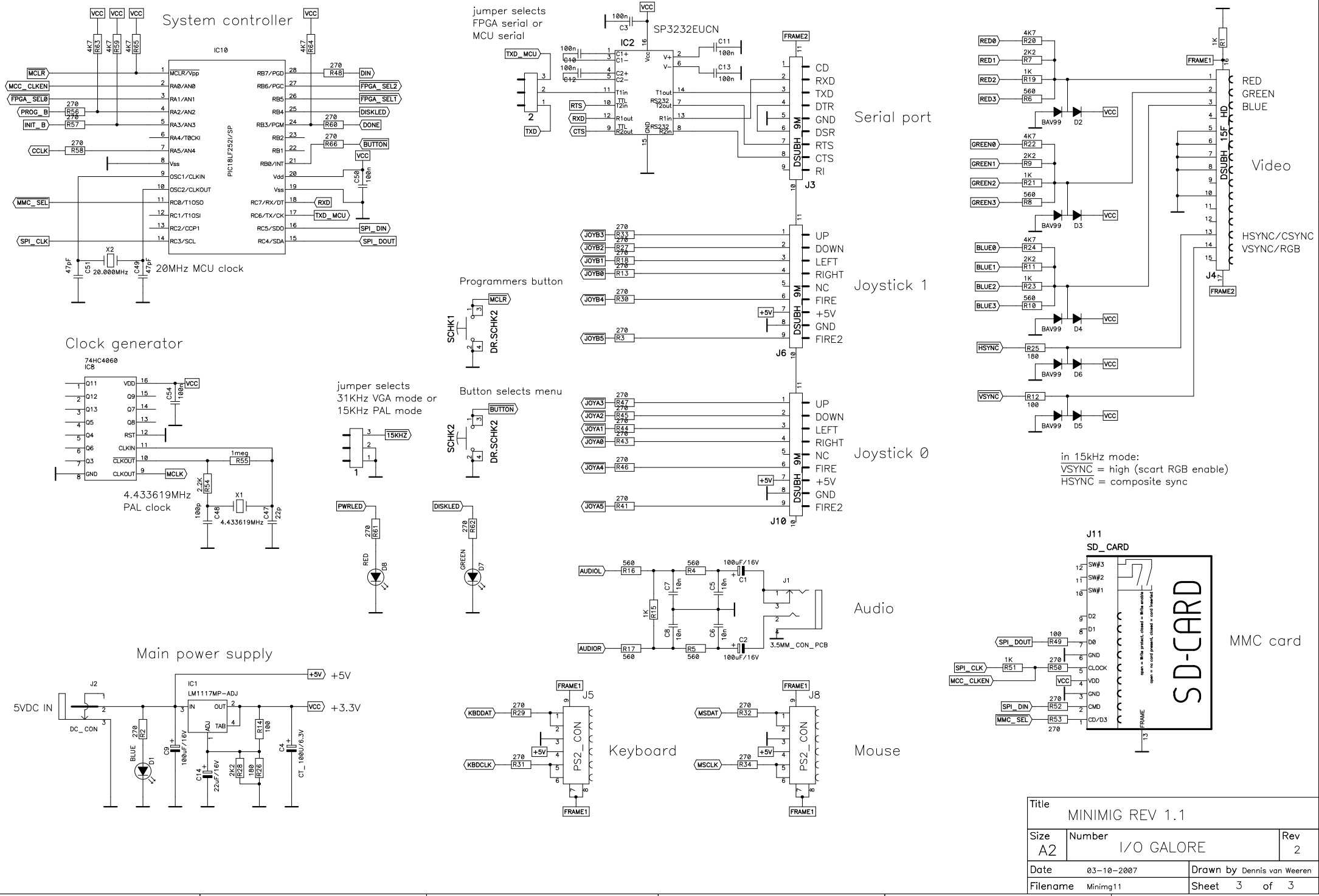








Title	MINIMIG REV 1.1	
Size A2	Number 68000 FOREVER & RAM	Rev 2
Date 03-10-2007		Drawn by Dennis van Weeren
Filename Minimg11		Sheet 2 of 3



Title	MINIMIG REV 1.1	
Size A2	Number I/O GALORE	Rev 2
Date 03-10-2007	Drawn by Dennis van Weeren	
Filename Minimg11	Sheet 3 of 3	